Printed-Tech-2.0

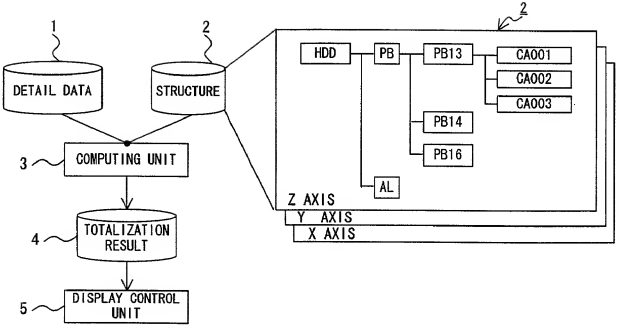
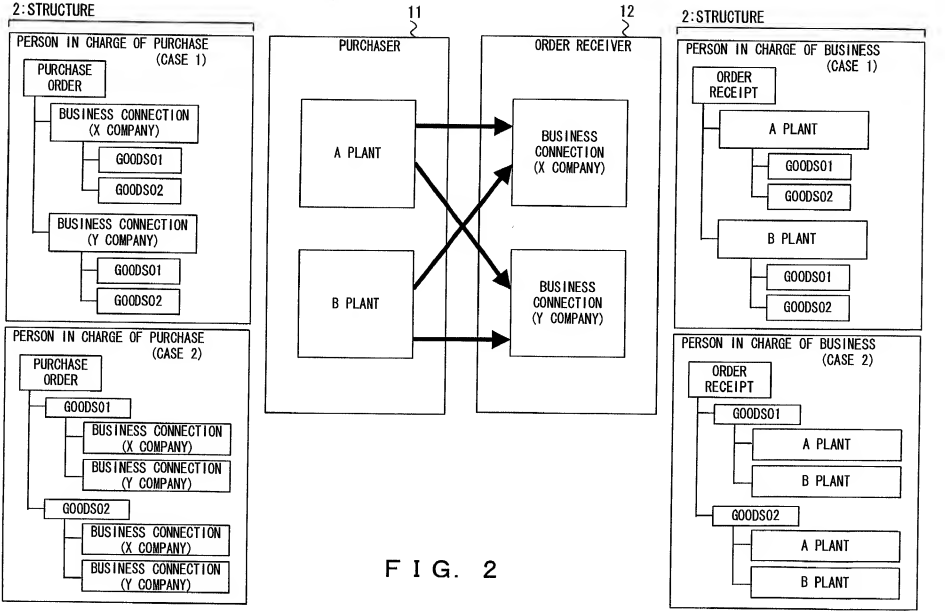


FIG. 1

TOT220'T66086O



F I G. 2

09/893,022

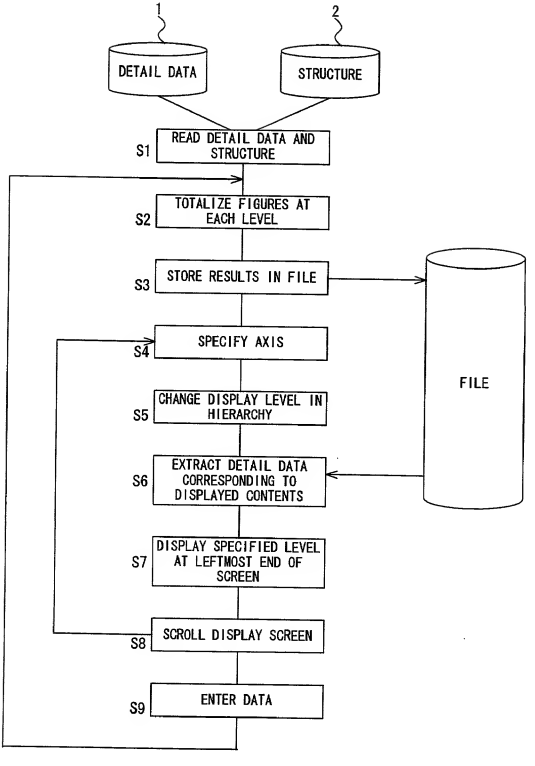


FIG. 3

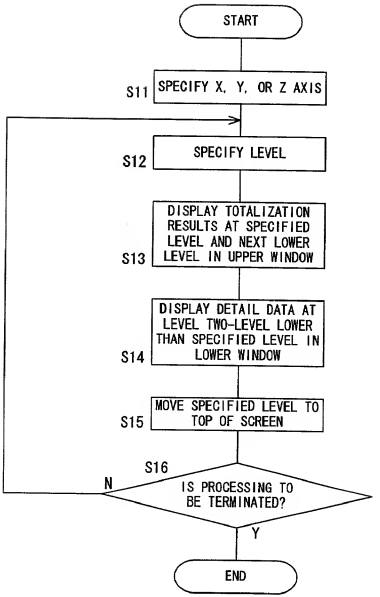

START
S11
SPECIFY X, Y, OR Z AXIS
S12
SPECIFY LEVEL
S13
DISPLAY TOTALIZATION RESULTS AT SPECIFIED LEVEL AND NEXT LOWER LEVEL IN UPPER WINDOW
S14
DISPLAY DETAIL DATA AT LEVEL TWO-LEVEL LOWER THAN SPECIFIED LEVEL IN LOWER WINDOW
S15
MOVE SPECIFIED LEVEL TO TOP OF SCREEN
S16
IS PROCESSING TO BE TERMINATED?
N
Y
END

FIG. 4

JP2012-252450

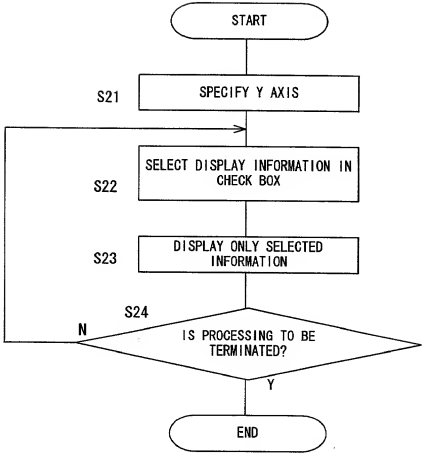


FIG. 5

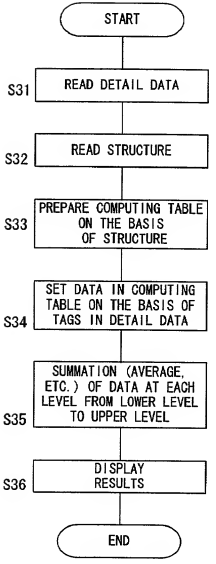

START
S31
READ DETAIL DATA
S32
READ STRUCTURE
S33
PREPARE COMPUTING TABLE ON THE BASIS OF STRUCTURE
S34
SET DATA IN COMPUTING TABLE ON THE BASIS OF TAGS IN DETAIL DATA
S35
SUMMATION (AVERAGE, ETC.) OF DATA AT EACH LEVEL FROM LOWER LEVEL TO UPPER LEVEL
S36
DISPLAY RESULTS
END

FIG. 6

PUB-2015-159940

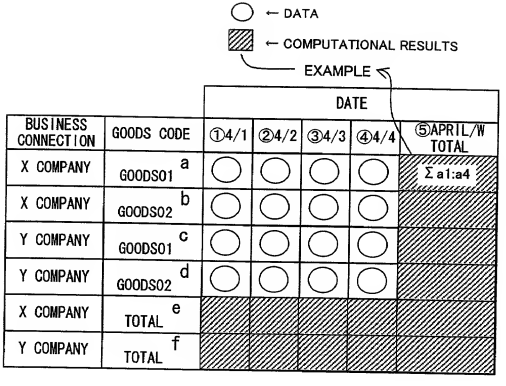


FIG. 7

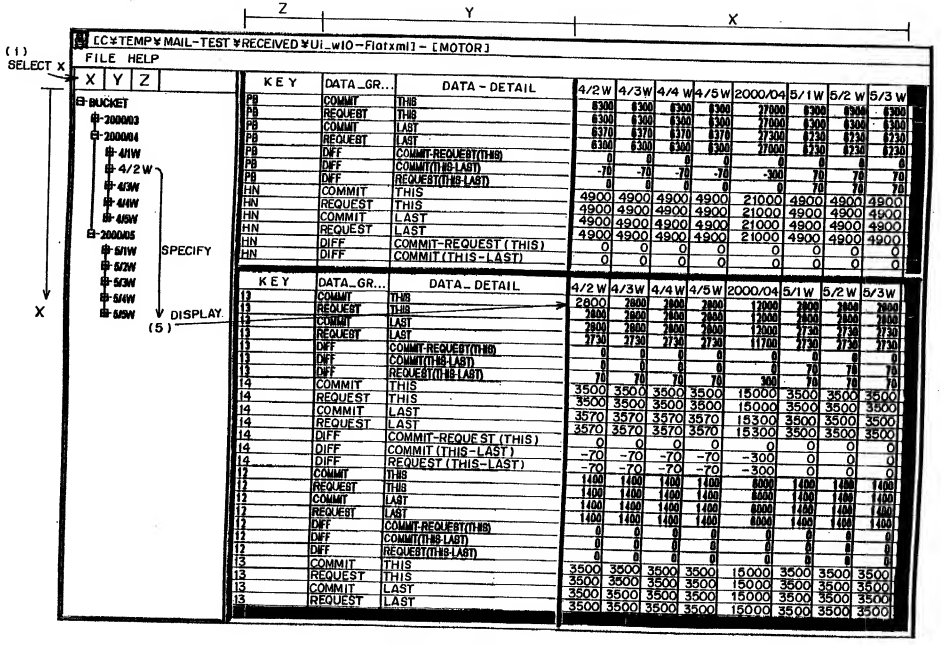
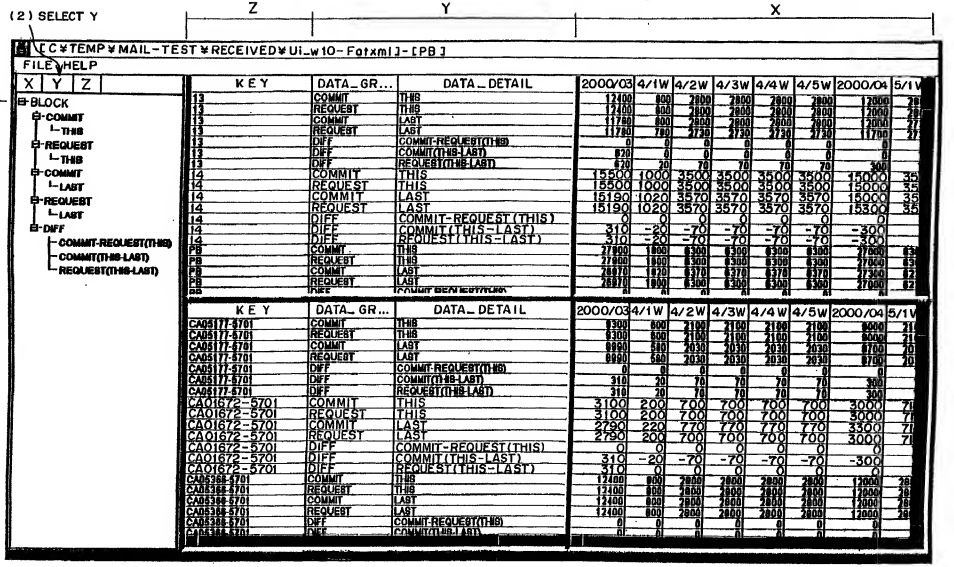

Z
Y
X
[C:¥TEMP¥MAIL-TEST¥RECEIVED¥UI_wIO-Flatxml] - [MOTOR]
FILE HELP
(1)
SELECT X
X Y Z
BUCKET
2000/03
2000/04
4/1W
4/2W
4/3W
4/4W
4/5W
2000/05
5/1W
5/2W
5/3W
5/4W
5/5W
SPECIFY
DISPLAY
(5)
KEY
DATA_GR...
DATA - DETAIL
4/2W
4/3W
4/4W
4/5W
2000/04
5/1W
5/2W
5/3W
PB COMMIT THIS 8300 8300 8300 8300 27000 8300 8300 8300
PB REQUEST THIS 6300 6300 6300 6300 27000 6300 6300 6300
PB COMMIT LAST 6370 6370 6370 6370 27300 6730 6730 6730
PB REQUEST LAST 6300 6300 6300 6300 27000 6730 6730 6730
PB DIFF COMMIT-REQUEST(THIS) 0 0 0 0 0 0 0 0
PB DIFF COMMIT(THIS-LAST) -70 -70 -70 -70 -300 70 70 70
PB DIFF REQUEST(THIS-LAST) 0 0 0 0 0 70 70 70
HN COMMIT THIS 4900 4900 4900 4900 21000 4900 4900 4900
HN REQUEST THIS 4900 4900 4900 4900 21000 4900 4900 4900
HN COMMIT LAST 4900 4900 4900 4900 21000 4900 4900 4900
HN REQUEST LAST 4900 4900 4900 4900 21000 4900 4900 4900
HN DIFF COMMIT-REQUEST (THIS) 0 0 0 0 0 0 0 0
HN DIFF COMMIT (THIS-LAST) 0 0 0 0 0 0 0 0
KEY
DATA_GR...
DATA_ DETAIL
4/2W
4/3W
4/4W
4/5W
2000/04
5/1W
5/2W
5/3W
13 COMMIT THIS 2800 2800 2800 2800 12000 2800 2800 2800
13 REQUEST THIS 2800 2800 2800 2800 12000 2800 2800 2800
13 COMMIT LAST 2800 2800 2800 2800 12000 2730 2730 2730
13 REQUEST LAST 2730 2730 2730 2730 11700 2730 2730 2730
13 DIFF COMMIT-REQUEST(THIS) 0 0 0 0 0 0 0 0
13 DIFF COMMIT(THIS-LAST) 0 0 0 0 0 70 70 70
13 DIFF REQUEST(THIS-LAST) 70 70 70 70 300 70 70 70
14 COMMIT THIS 3500 3500 3500 3500 15000 3500 3500 3500
14 REQUEST THIS 3500 3500 3500 3500 15000 3500 3500 3500
14 COMMIT LAST 3570 3570 3570 3570 15300 3500 3500 3500
14 REQUEST LAST 3570 3570 3570 3570 15300 3500 3500 3500
14 DIFF COMMIT-REQUEST (THIS) 0 0 0 0 0 0 0 0
14 DIFF COMMIT (THIS-LAST) -70 -70 -70 -70 -300 0 0 0
14 DIFF REQUEST (THIS-LAST) -70 -70 -70 -70 -300 0 0 0
12 COMMIT THIS 1400 1400 1400 1400 8000 1400 1400 1400
12 REQUEST THIS 1400 1400 1400 1400 8000 1400 1400 1400
12 COMMIT LAST 1400 1400 1400 1400 8000 1400 1400 1400
12 REQUEST LAST 1400 1400 1400 1400 8000 1400 1400 1400
12 DIFF COMMIT-REQUEST(THIS) 0 0 0 0 0 0 0 0
12 DIFF COMMIT(THIS-LAST) 0 0 0 0 0 0 0 0
12 DIFF REQUEST(THIS-LAST) 0 0 0 0 0 0 0 0
13 COMMIT THIS 3500 3500 3500 3500 15000 3500 3500 3500
13 REQUEST THIS 3500 3500 3500 3500 15000 3500 3500 3500
13 COMMIT LAST 3500 3500 3500 3500 15000 3500 3500 3500
13 REQUEST LAST 3500 3500 3500 3500 15000 3500 3500 3500

FIG. 8

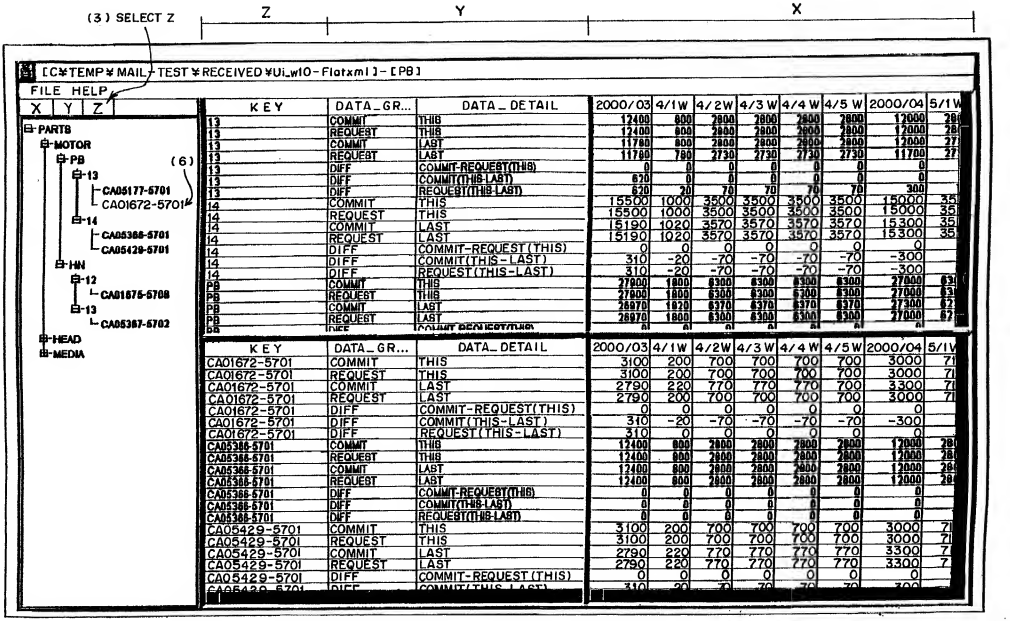


KEY	DATA_GR...	DATA_DETAIL	2000/03	4/1W	4/2W	4/3W	4/4W	4/5W	2000/04	5/1W
13	COMMIT	THIB	12400	800	2800	2800	2800	2800	12000	28
13	REQUEST	THIB	12400	800	2800	2800	2800	2800	12000	28
13	COMMIT	LAST	11780	800	2800	2800	2800	2800	12000	27
13	REQUEST	LAST	11780	780	2730	2730	2730	2730	11700	27
13	DIFF	COMMIT-REQUEST(THIB)	0	0	0	0	0	0	0	
13	DIFF	COMMIT(THIB-LAST)	620	0	0	0	0	0	0	
13	DIFF	REQUEST(THIB-LAST)	620	20	70	70	70	70	300	
14	COMMIT	THIS	15500	1000	3500	3500	3500	3500	15000	35
14	REQUEST	THIS	15500	1000	3500	3500	3500	3500	15000	35
14	COMMIT	LAST	15190	1020	3570	3570	3570	3570	15000	35
14	REQUEST	LAST	15190	1020	3570	3570	3570	3570	15300	35
14	DIFF	COMMIT-REQUEST(THIS)	0	0	0	0	0	0	0	
14	DIFF	COMMIT(THIS-LAST)	310	-20	-70	-70	-70	-70	-300	
14	DIFF	REQUEST(THIS-LAST)	310	-20	-70	-70	-70	-70	-300	
PB	COMMIT	THIS	27900	1800	6300	6300	6300	6300	27000	63
PB	REQUEST	THIS	27900	1800	6300	6300	6300	6300	27000	63
PB	COMMIT	LAST	26970	1820	6370	6370	6370	6370	27300	63
PB	REQUEST	LAST	26970	1800	6300	6300	6300	6300	27000	63
PB	DIFF	[illegible]	0	0	0	0	0	0	0	

KEY	DATA_GR...	DATA_DETAIL	2000/03	4/1W	4/2W	4/3W	4/4W	4/5W	2000/04	5/1W
CA05177-5701	COMMIT	THIB	8300	600	2100	2100	2100	2100	8000	21
CA05177-5701	REQUEST	THIB	8300	600	2100	2100	2100	2100	8000	21
CA05177-5701	COMMIT	LAST	8990	580	2030	2030	2030	2030	8700	20
CA05177-5701	REQUEST	LAST	8990	580	2030	2030	2030	2030	8700	20
CA05177-5701	DIFF	COMMIT-REQUEST(THIB)	0	0	0	0	0	0	0	
CA05177-5701	DIFF	COMMIT(THIB-LAST)	310	20	70	70	70	70	300	
CA05177-5701	DIFF	REQUEST(THIB-LAST)	310	20	70	70	70	70	300	
CA01672-5701	COMMIT	THIS	3100	200	700	700	700	700	3000	70
CA01672-5701	REQUEST	THIS	3100	200	700	700	700	700	3000	70
CA01672-5701	COMMIT	LAST	2790	220	770	770	770	770	3300	70
CA01672-5701	REQUEST	LAST	2790	200	700	700	700	700	3000	70
CA01672-5701	DIFF	COMMIT-REQUEST(THIS)	0	0	0	0	0	0	0	
CA01672-5701	DIFF	COMMIT(THIS-LAST)	310	-20	-70	-70	-70	-70	-300	
CA01672-5701	DIFF	REQUEST(THIS-LAST)	310	0	0	0	0	0	0	
CA05388-5701	COMMIT	THIB	12400	800	2800	2800	2800	2800	12000	28
CA05388-5701	REQUEST	THIB	12400	800	2800	2800	2800	2800	12000	28
CA05388-5701	COMMIT	LAST	12400	800	2800	2800	2800	2800	12000	28
CA05388-5701	REQUEST	LAST	12400	800	2800	2800	2800	2800	12000	28
CA05388-5701	DIFF	COMMIT-REQUEST(THIB)	0	0	0	0	0	0	0	
CA05388-5701	DIFF	COMMIT(THIB-LAST)	0	0	0	0	0	0	0	

FIG. 9

TOT220'T6E88260



KEY	DATA_GR...	DATA_DETAIL	2000/03	4/1W	4/2W	4/3W	4/4W	4/5W	2000/04	5/1W
13	COMMIT	THIS	12400	800	2800	2800	2800	2800	12000	28
13	REQUEST	THIS	12400	800	2800	2800	2800	2800	12000	28
13	COMMIT	LAST	11780	800	2800	2800	2800	2800	12000	27
13	REQUEST	LAST	11780	780	2730	2730	2730	2730	11700	27
13	DIFF	COMMIT-REQUEST(THIS)	0	0	0	0	0	0	0	
13	DIFF	COMMIT(THIS-LAST)	620	0	0	0	0	0	0	
13	DIFF	REQUEST(THIS-LAST)	620	20	70	70	70	70	300	
14	COMMIT	THIS	15500	1000	3500	3500	3500	3500	15000	35
14	REQUEST	THIS	15500	1000	3500	3500	3500	3500	15000	35
14	COMMIT	LAST	15190	1020	3570	3570	3570	3570	15300	35
14	REQUEST	LAST	15190	1020	3570	3570	3570	3570	15300	35
14	DIFF	COMMIT-REQUEST(THIS)	0	0	0	0	0	0	0	
14	DIFF	COMMIT(THIS-LAST)	310	-20	-70	-70	-70	-70	-300	
14	DIFF	REQUEST(THIS-LAST)	310	-20	-70	-70	-70	-70	-300	
PB	COMMIT	THIS	27900	1800	6300	6300	6300	6300	27000	63
PB	REQUEST	THIS	27900	1800	6300	6300	6300	6300	27000	63
PB	COMMIT	LAST	26970	1820	6370	6370	6370	6370	27300	63
PB	REQUEST	LAST	26970	1800	6300	6300	6300	6300	27000	63
PB	DIFF	COMMIT-REQUEST(THIS)	0	0	0	0	0	0	0	

KEY	DATA_GR...	DATA_DETAIL	2000/03	4/1W	4/2W	4/3W	4/4W	4/5W	2000/04	5/1W
CA01672-5701	COMMIT	THIS	3100	200	700	700	700	700	3000	7
CA01672-5701	REQUEST	THIS	3100	200	700	700	700	700	3000	7
CA01672-5701	COMMIT	LAST	2790	220	770	770	770	700	3300	7
CA01672-5701	REQUEST	LAST	2790	200	700	700	700	700	3000	7
CA01672-5701	DIFF	COMMIT-REQUEST(THIS)	0	0	0	0	0	0	0	
CA01672-5701	DIFF	COMMIT(THIS-LAST)	310	-20	-70	-70	-70	-70	-300	
CA01672-5701	DIFF	REQUEST(THIS-LAST)	310	0	0	0	0	0	0	
CA05368-5701	COMMIT	THIS	12400	800	2800	2800	2800	2800	12000	28
CA05368-5701	REQUEST	THIS	12400	800	2800	2800	2800	2800	12000	28
CA05368-5701	COMMIT	LAST	12400	800	2800	2800	2800	2800	12000	28
CA05368-5701	REQUEST	LAST	12400	800	2800	2800	2800	2800	12000	28
CA05368-5701	DIFF	COMMIT-REQUEST(THIS)	0	0	0	0	0	0	0	
CA05368-5701	DIFF	COMMIT(THIS-LAST)	0	0	0	0	0	0	0	
CA05368-5701	DIFF	REQUEST(THIS-LAST)	0	0	0	0	0	0	0	
CA05429-5701	COMMIT	THIS	3100	200	700	700	700	700	3000	7
CA05429-5701	REQUEST	THIS	3100	200	700	700	700	700	3000	7
CA05429-5701	COMMIT	LAST	2790	220	770	770	770	770	3300	7
CA05429-5701	REQUEST	LAST	2790	220	770	770	770	770	3300	7
CA05429-5701	DIFF	COMMIT-REQUEST(THIS)	0	0	0	0	0	0	0	
CA05429-5701	DIFF	COMMIT(THIS-LAST)	310	-20	-70	-70	-70	-70	-300	

FIG. 10

TOT220' T6E98460

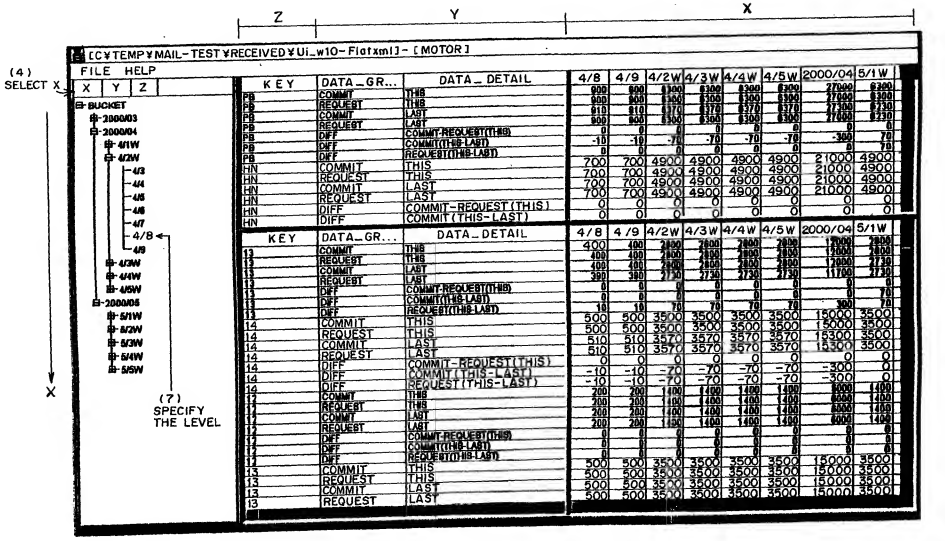


FIG. 11

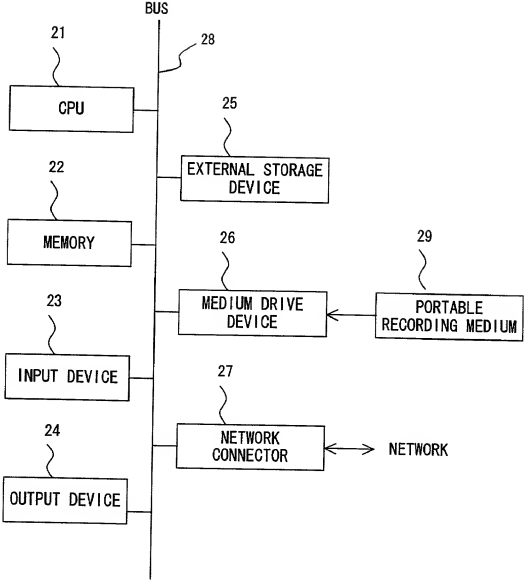

BUS
21
28
CPU
25
EXTERNAL STORAGE DEVICE
22
MEMORY
26
29
MEDIUM DRIVE DEVICE
PORTABLE RECORDING MEDIUM
23
INPUT DEVICE
27
24
NETWORK CONNECTOR
NETWORK
OUTPUT DEVICE

FIG. 12

EDITED BY T66888760

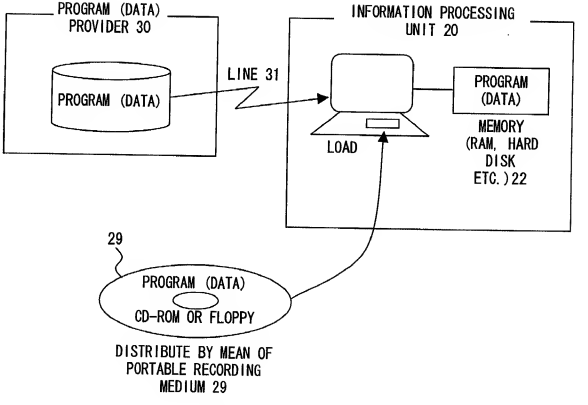


FIG. 13